

SIM4LE	185		
EDC15C5	15		
EDC15C6	17		
EDC15C5	15		
EDC15C6	17		
ME2.8	154	Boot(ST)	622
ME2.8	154	Boot(ST)	622
EDC15C6	17		
ME2.8	154	Boot(ST)	622
EDC15C6	17		
ME2.0	142		
ME2.0	142		
ME2.8	154	Boot(ST)	622
ME2.0	142		
ME2.8	154		
ME2.0	142		
SIM4LKE	199		
SIM4LKE	199		
EDC16C2	61	Bdm(MPC5xx)	4
EDC16C31	61	Bdm(MPC5xx)	4
EDC16C2	61	Bdm(MPC5xx)	4
EDC16C31	61	Bdm(MPC5xx)	4
DCM3.2		Bdm(MPC5xx)	119
ME2.8	154	Boot(ST)	622
EDC16C2	61	Bdm(MPC5xx)	4
EDC16CP31	69	Bdm(MPC5xx)	71
ME9.7	312	Bdm(MPC5xx)	102
EDC16C2	61	Bdm(MPC5xx)	4
EDC16CP31	69	Bdm(MPC5xx)	71
EDC16C2	61	Bdm(MPC5xx)	4
ME2.8	154	Boot(ST)	622
ME9.7	312	Bdm(MPC5xx)	102
EDC16CP36	69	Bdm(MPC5xx)	71
ME2.8	154	Boot(ST)	622
ME9.7	312	Bdm(MPC5xx)	102
ME2.8	154		
DCM3.5	294	Jtag(Nexus)	248
DCM3.5	294	Jtag(Nexus)	248
CRD 3.XX	350	Boot(Tricore)	1808
CRD 3.XX	350	Boot(Tricore)	1808
CRD 3.XX	350	Boot(Tricore)	1808
DCM3.5	294	Jtag(Nexus)	248
EDC17CP01		Boot(Tricore)	1155
EDC17CP01		Boot(Tricore)	1155
EDC17CP01		Boot(Tricore)	1155
EDC17CP01		Boot(Tricore)	1155
EDC17CP01		Boot(Tricore)	1155
ME9.7	312	Bdm(MPC5xx)	102
ME9.7	312	Bdm(MPC5xx)	102
MED17.7.1		Boot(Tricore)	1249